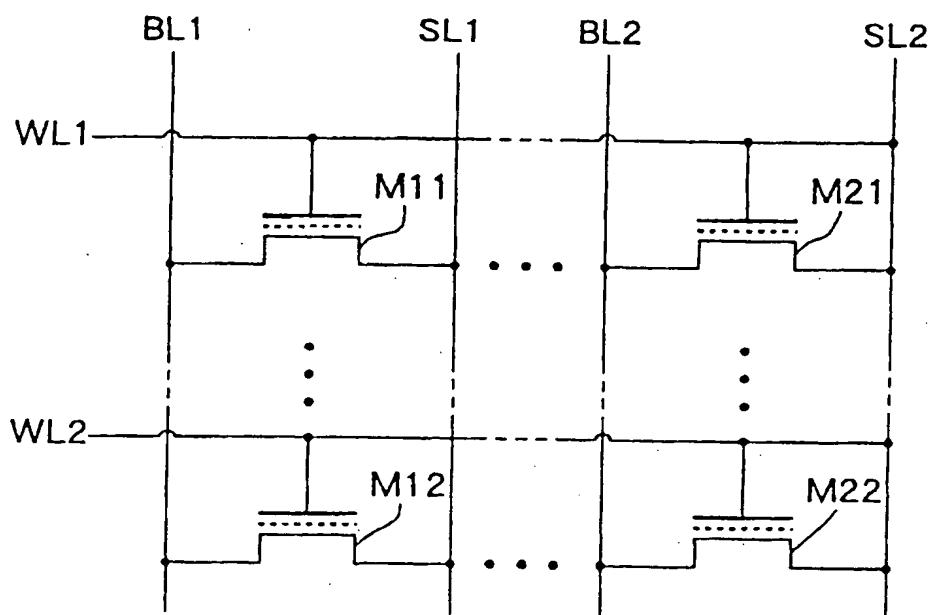


METHOD OF ERASING NON-VOLATILE SEMICONDUCTOR MEMORY
DEVICE AND SUCH NON-VOLATILE SEMICONDUCTOR MEMORY DEVICE
Inventor: Ichiro Fujiwara
Docket No. 075834.00465

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FIG.1

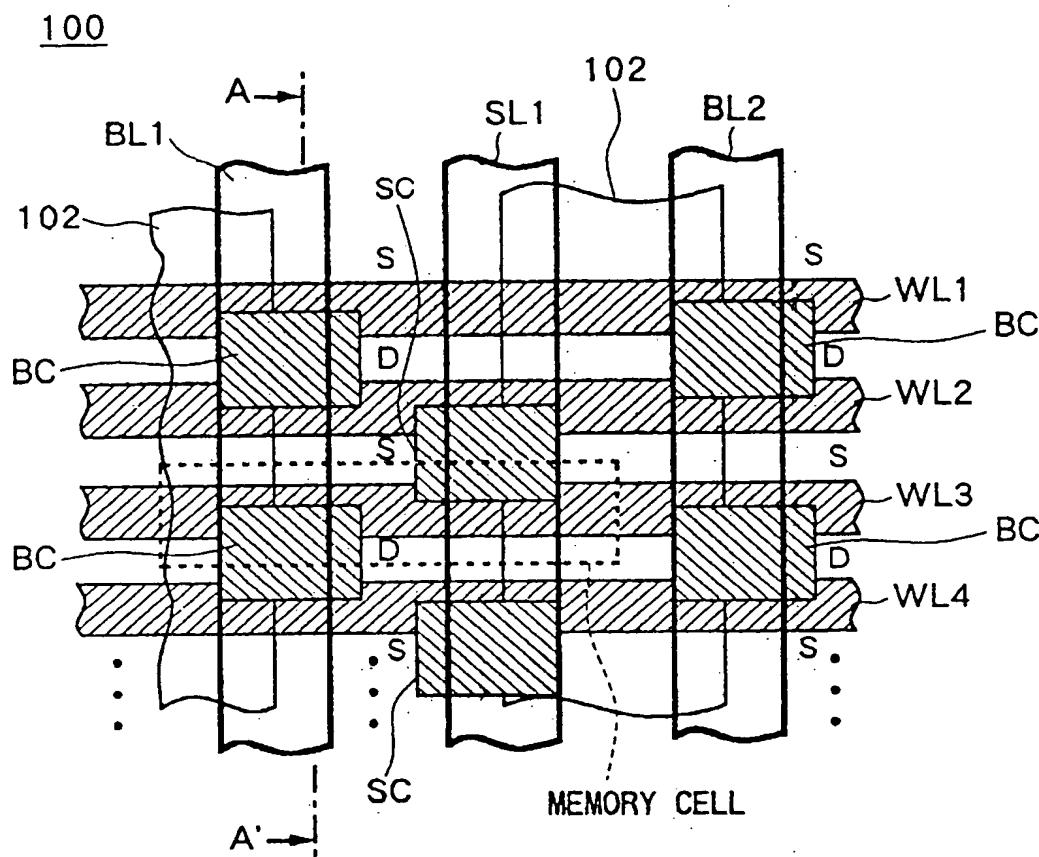


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FIG.2

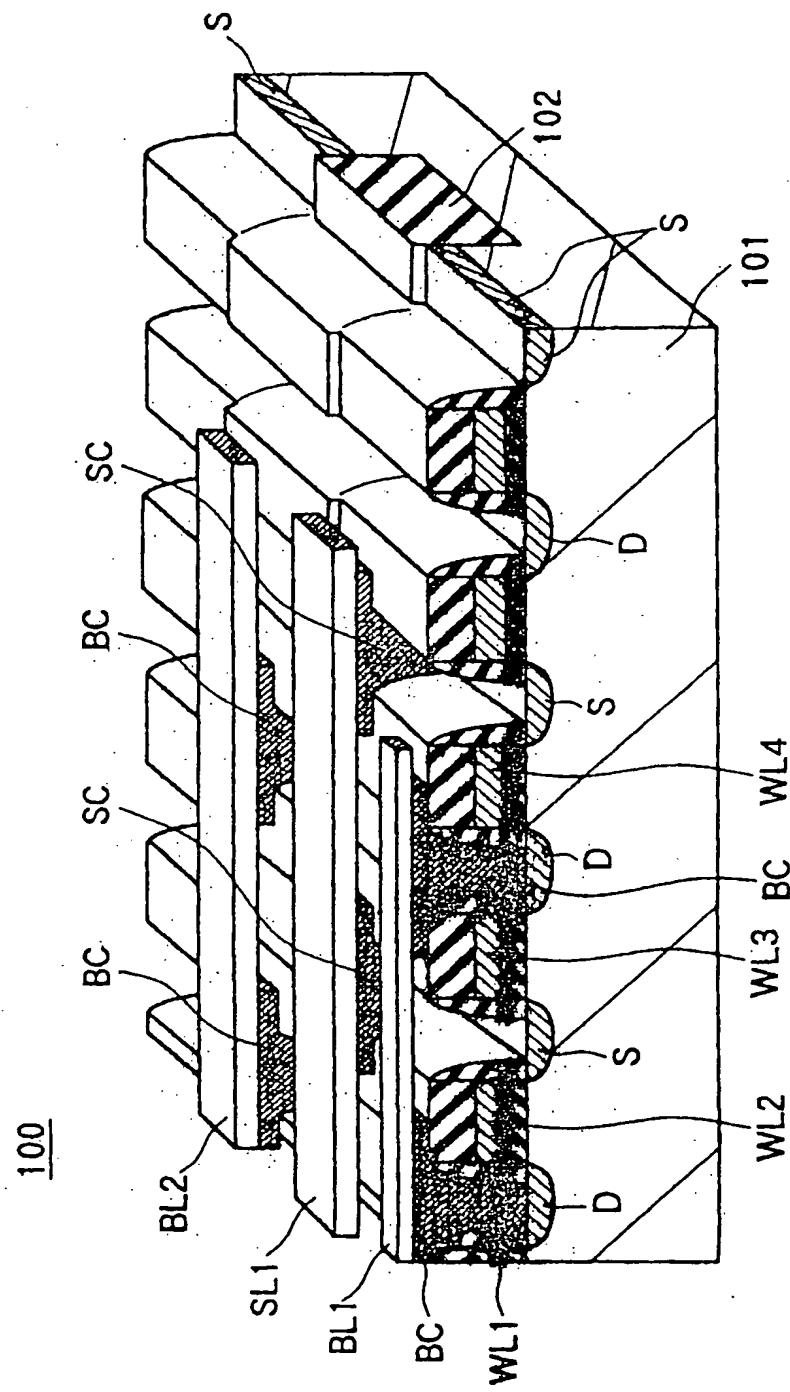


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FIG.3

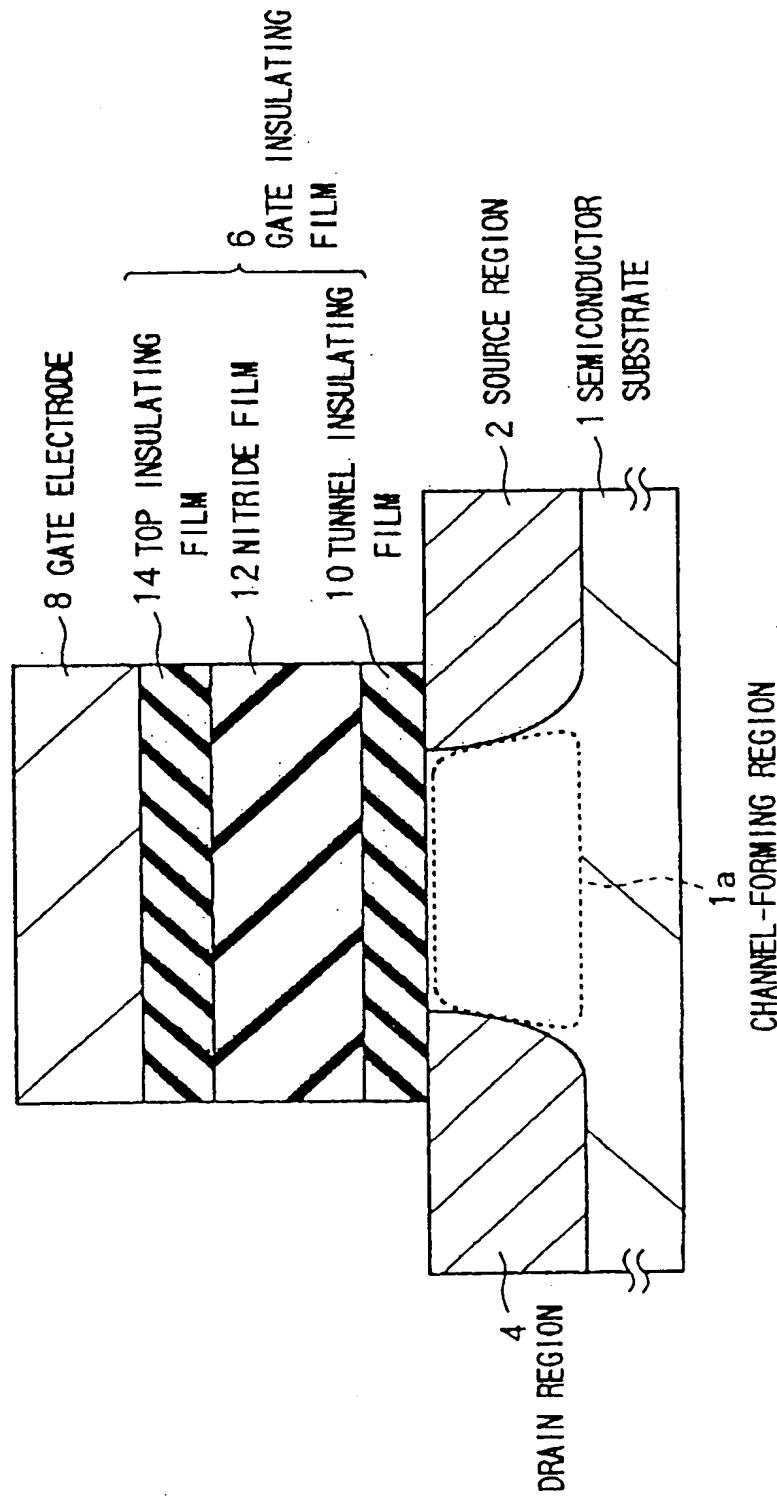


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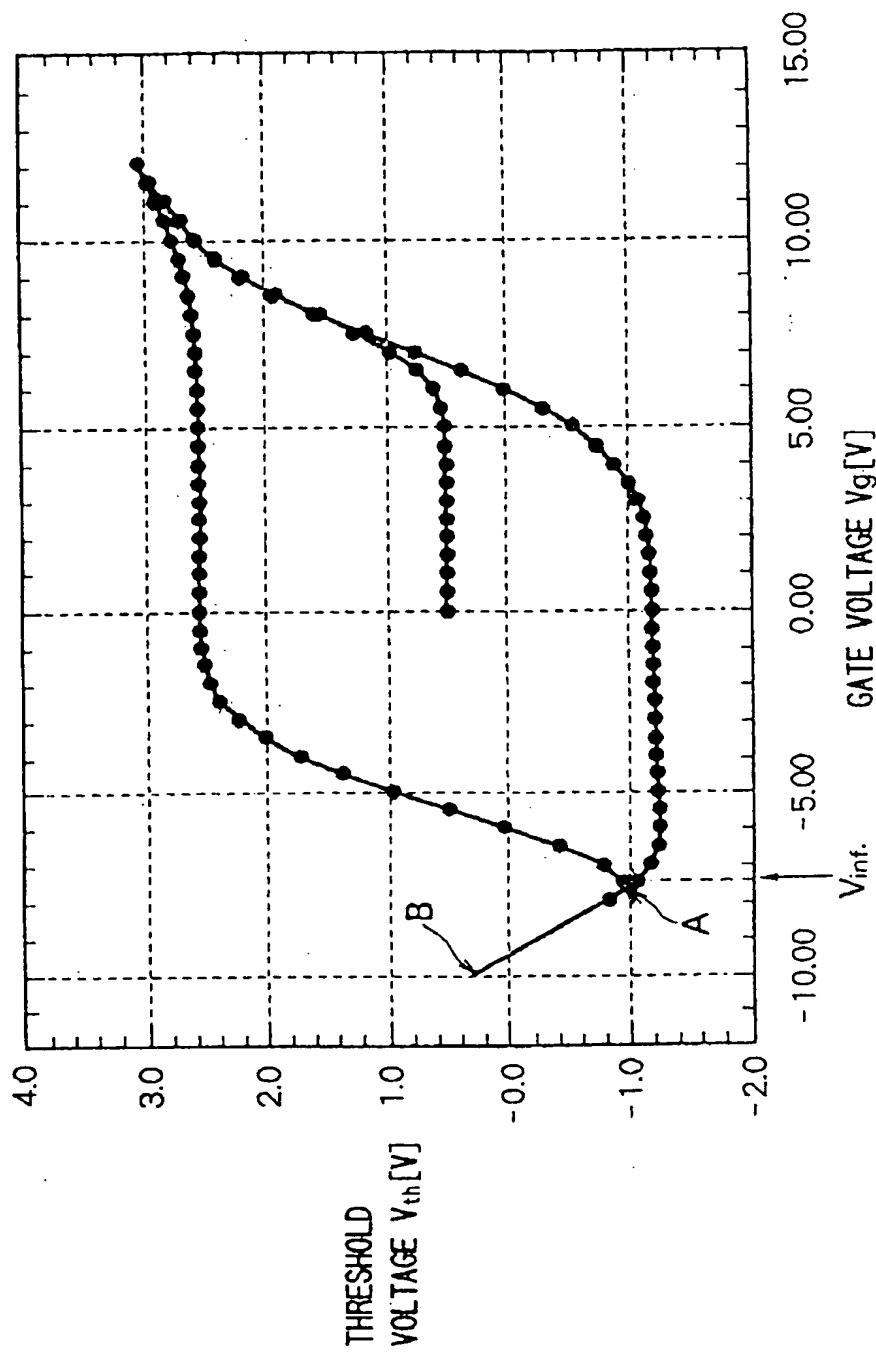
FIG.4



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FIG. 5
MEMORY HYSTERESIS CHARACTERISTIC

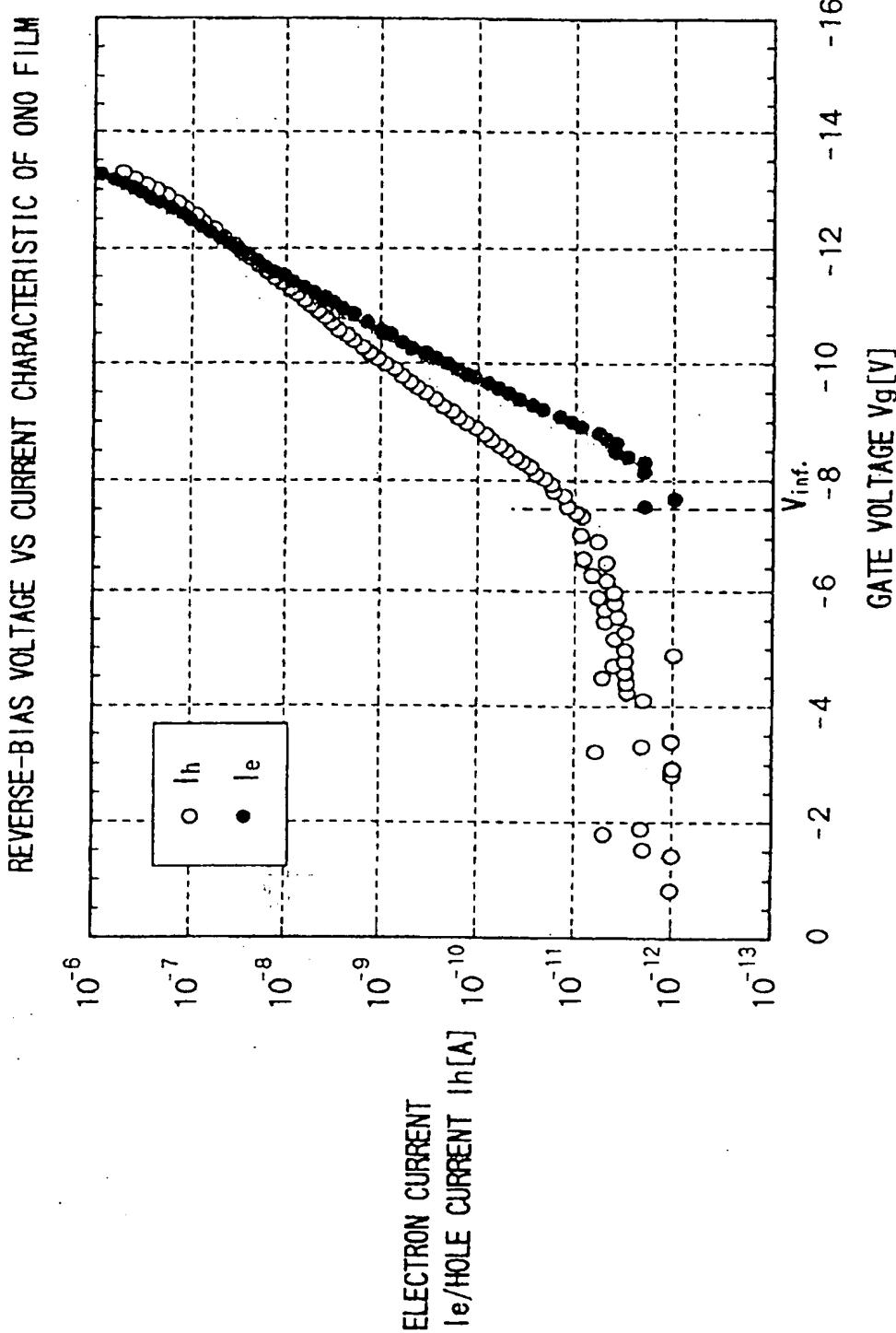


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FIG.6.

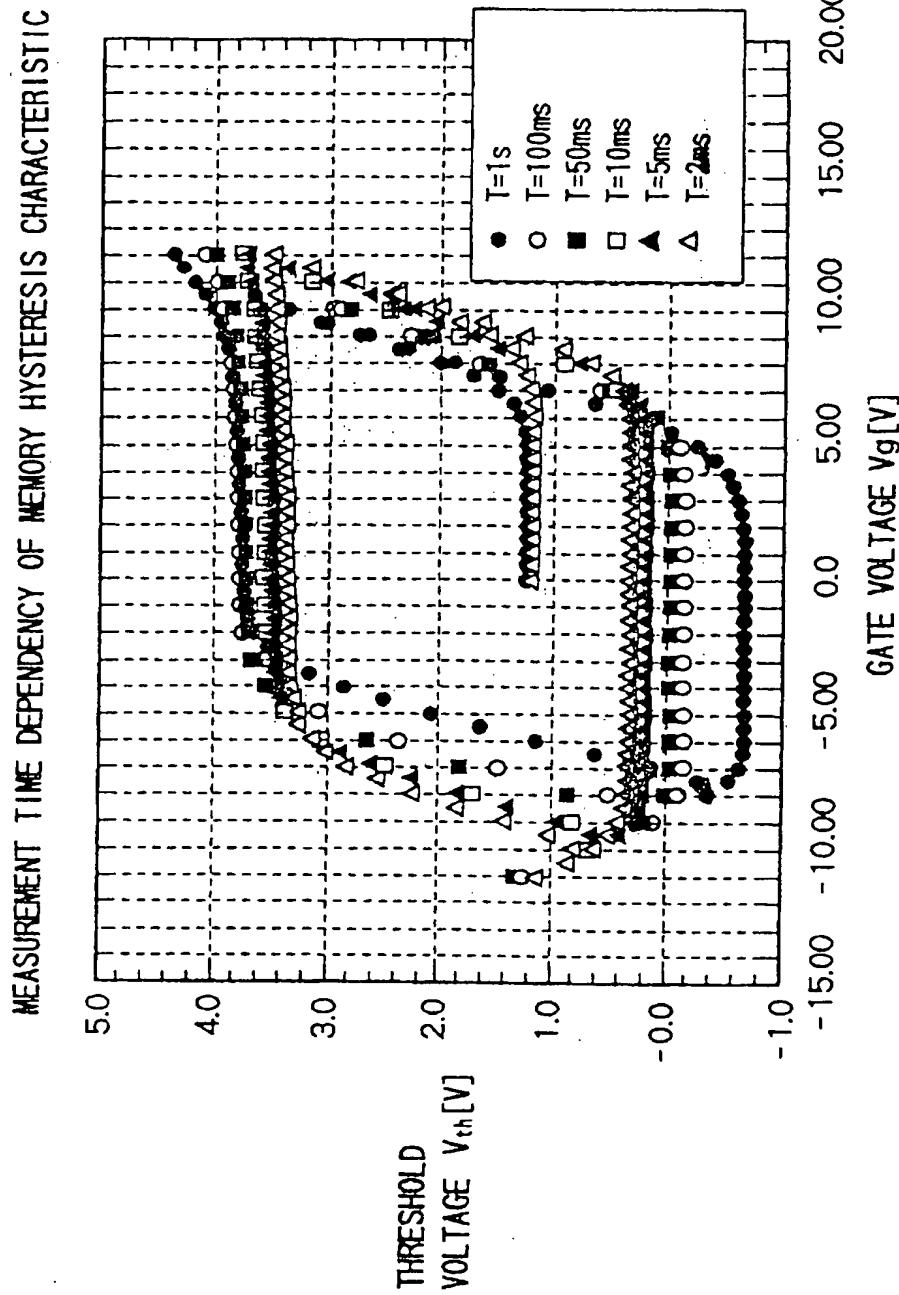


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FIG.7

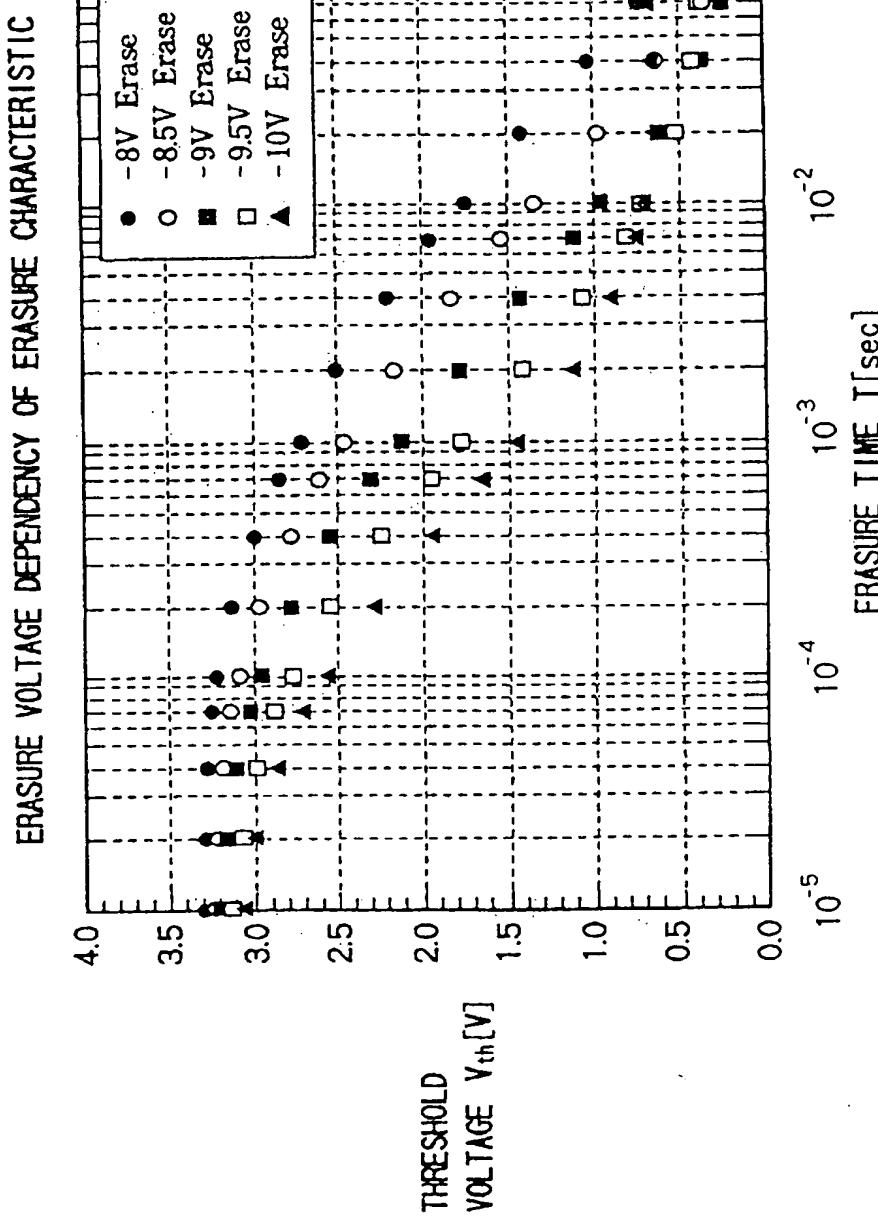


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FIG.8

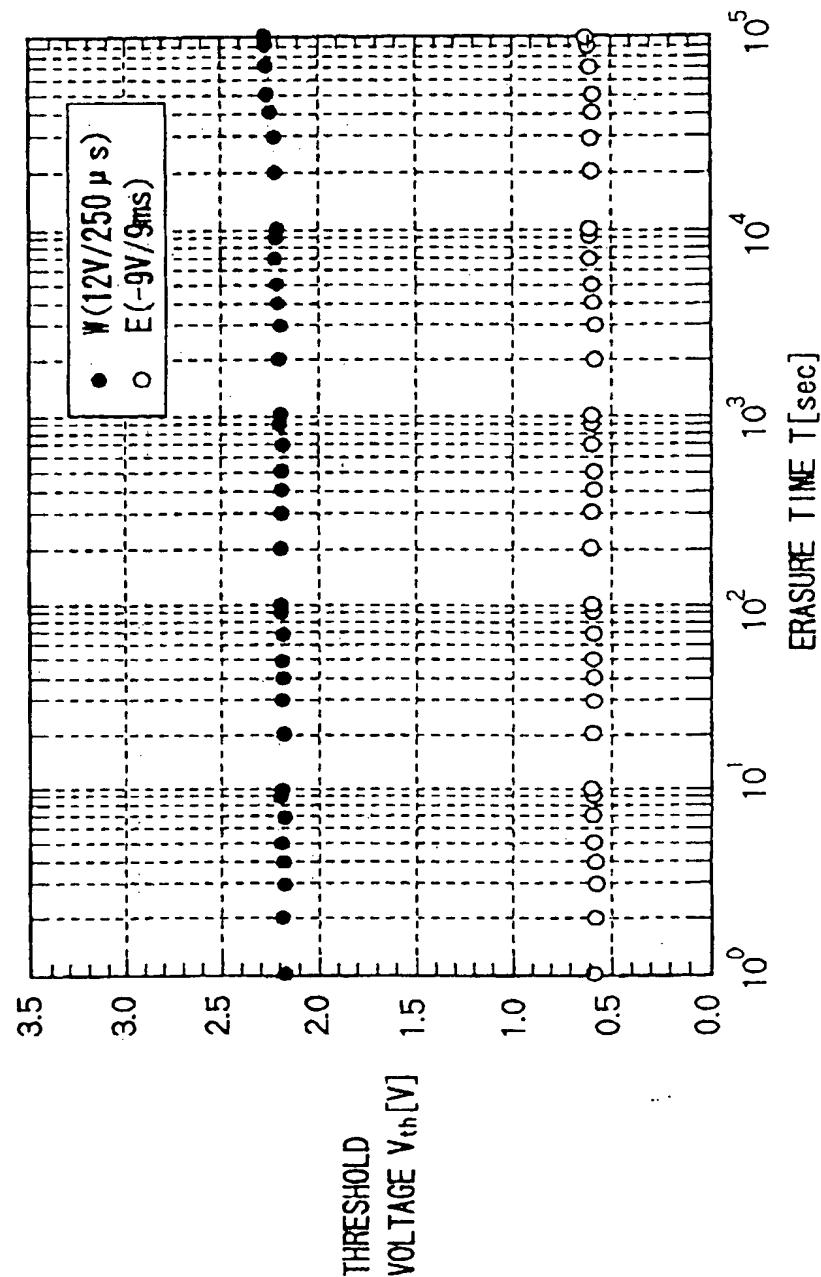


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FIG.9

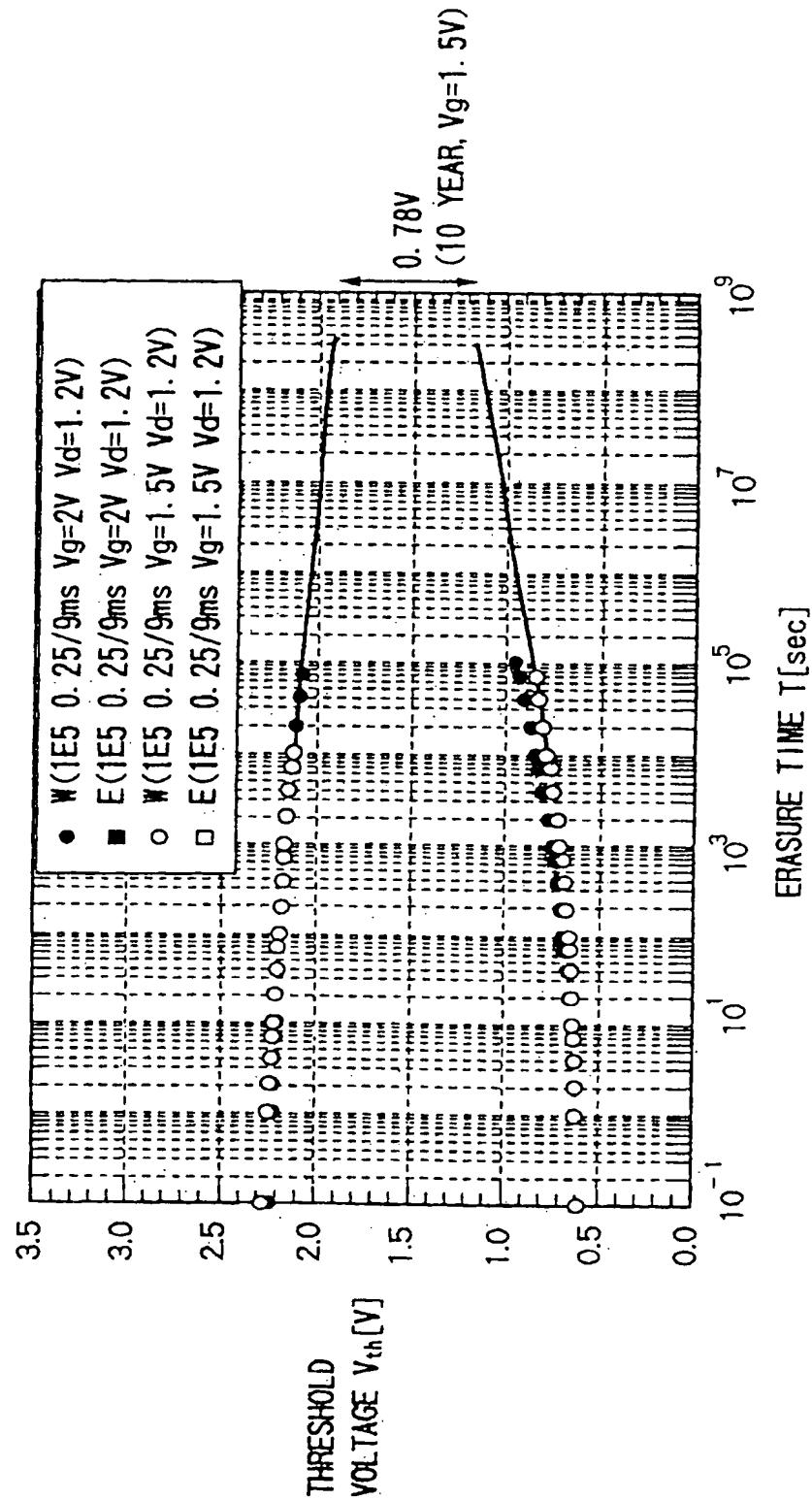


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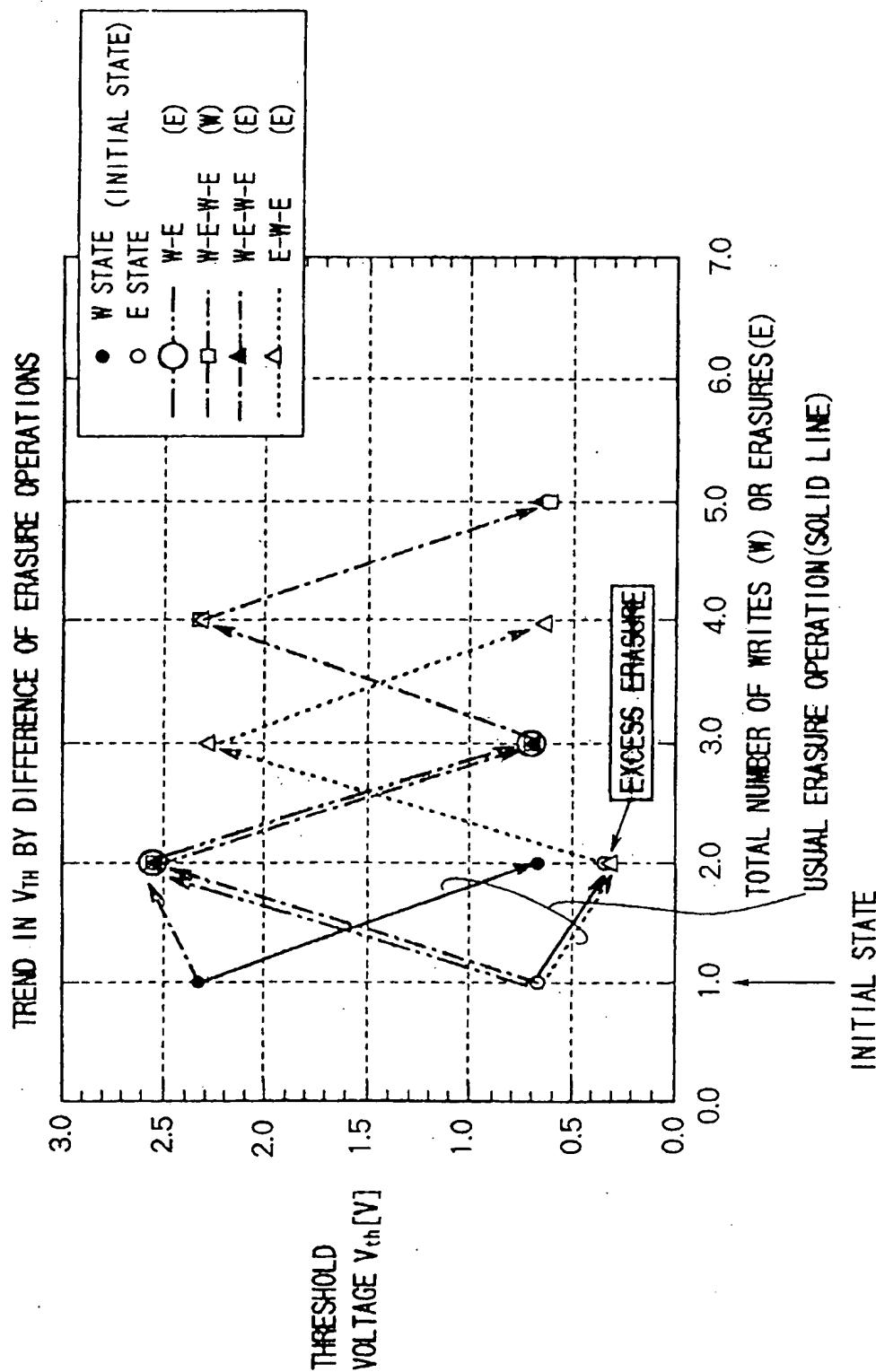
FIG. 10



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FIG. 11

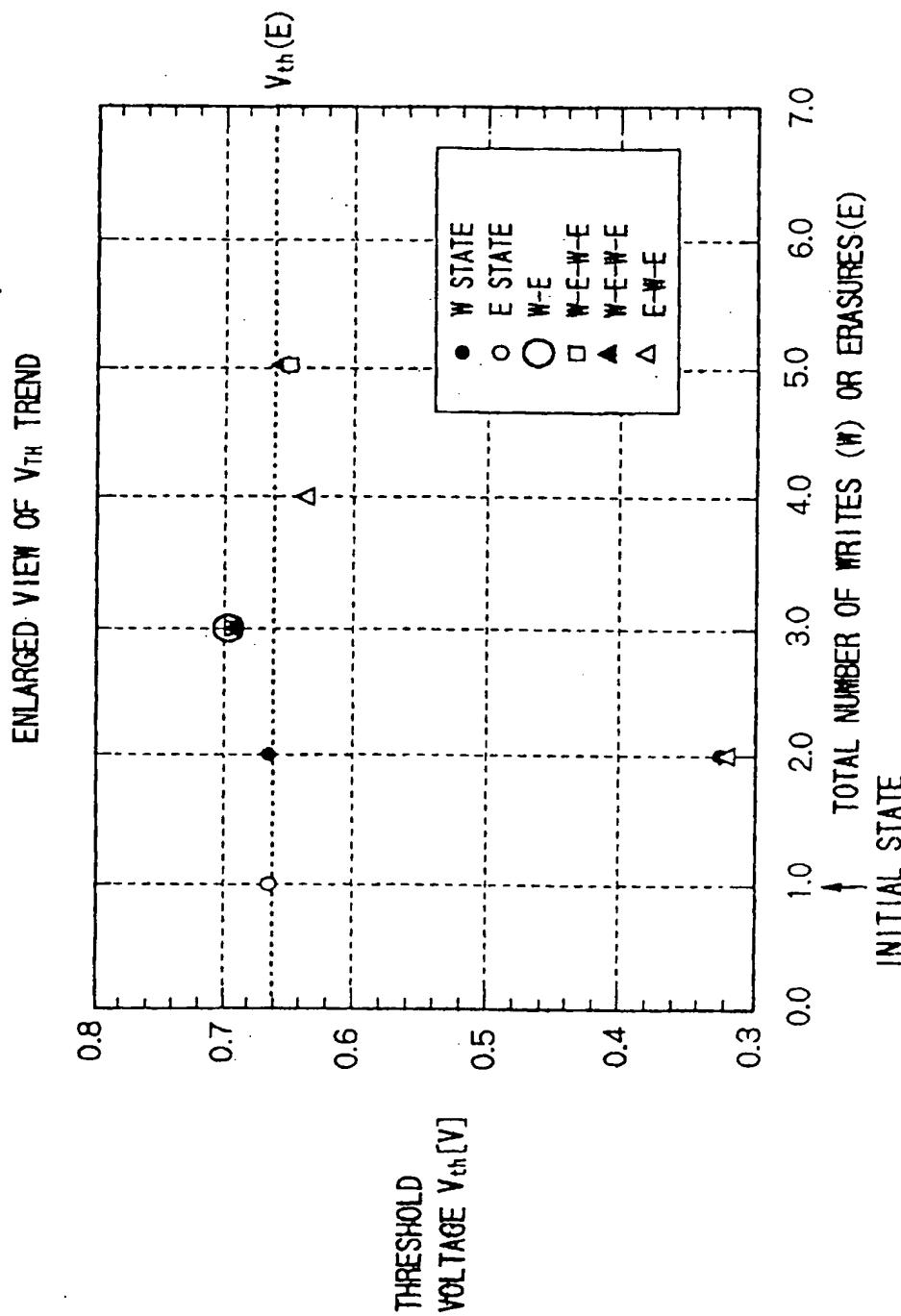


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FIG.12



METHOD OF ERASING NON-VOLATILE SEMICONDUCTOR MEMORY
DEVICE AND SUCH NON-VOLATILE SEMICONDUCTOR MEMORY DEVICE

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ERASURE OPERATION ACCORDING TO PRESENT INVENTION

FIG.13A

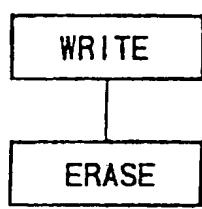


FIG. 13B

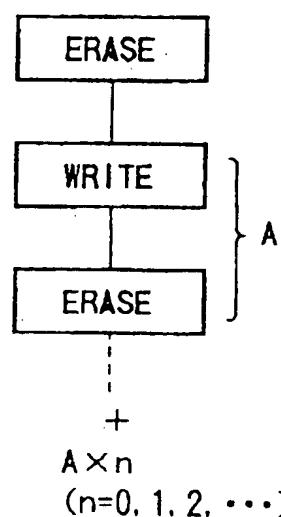
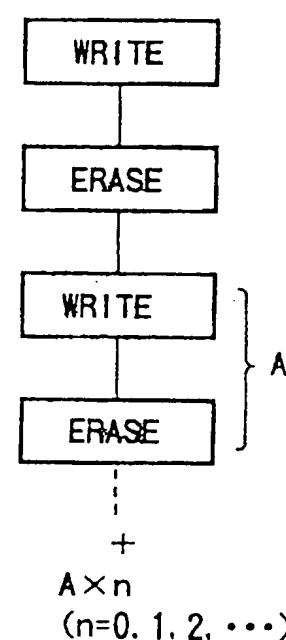


FIG. 13C

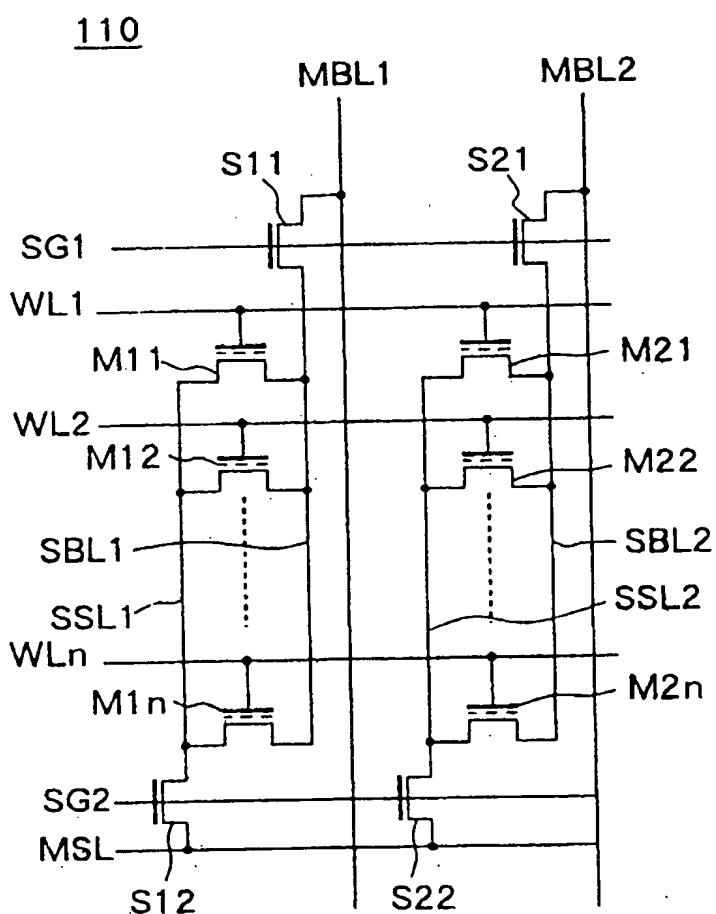


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FIG.14



METHOD OF ERASING NON-VOLATILE SEMICONDUCTOR MEMORY DEVICE AND SUCH NON-VOLATILE SEMICONDUCTOR MEMORY DEVICE

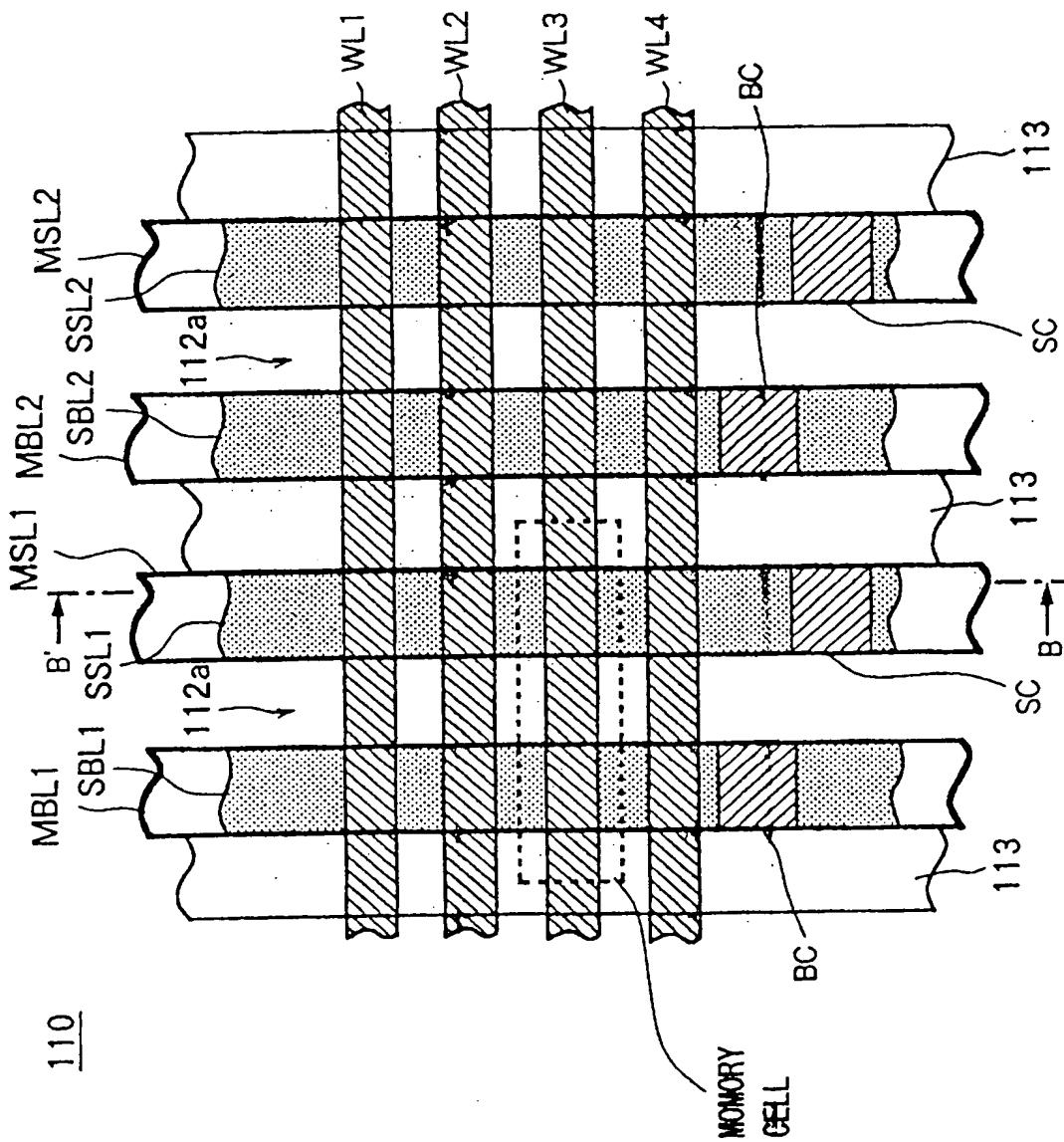
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FIG.

二〇

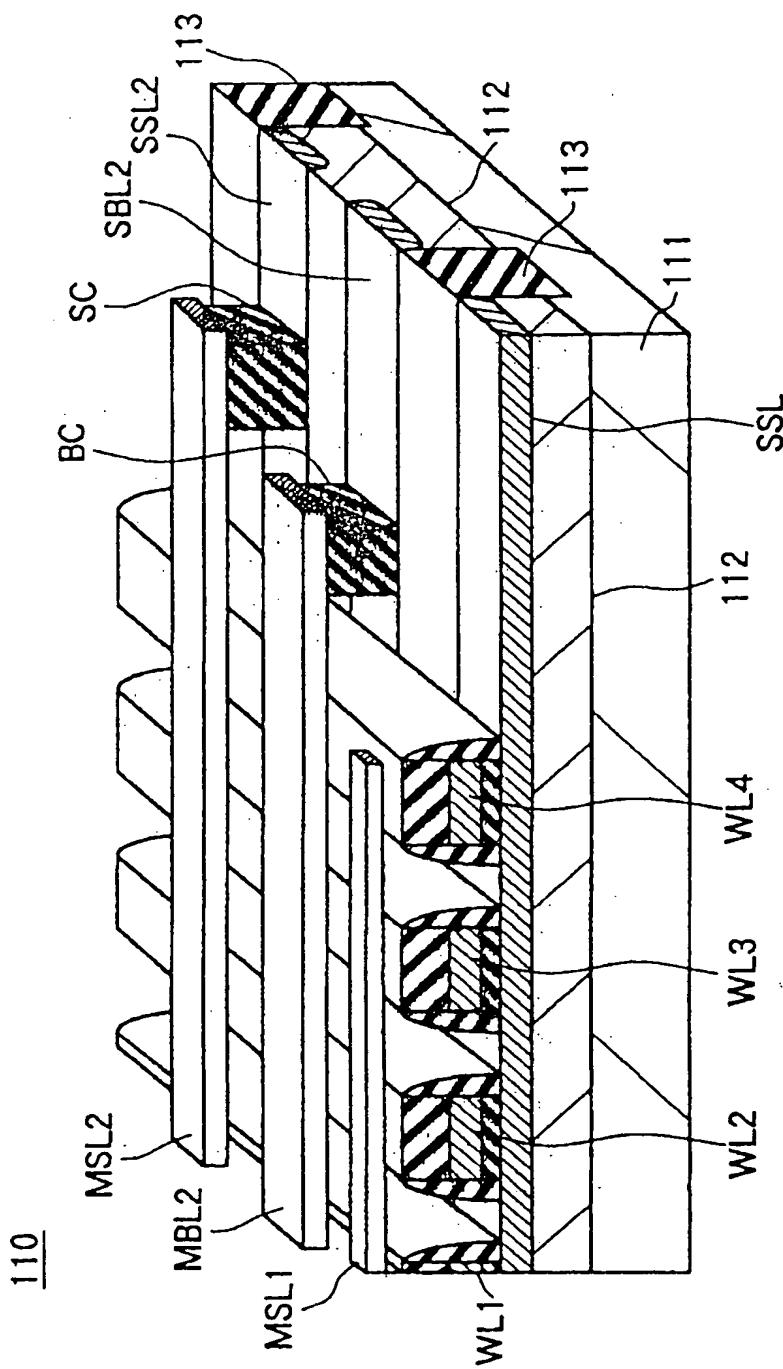


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FIG. 16

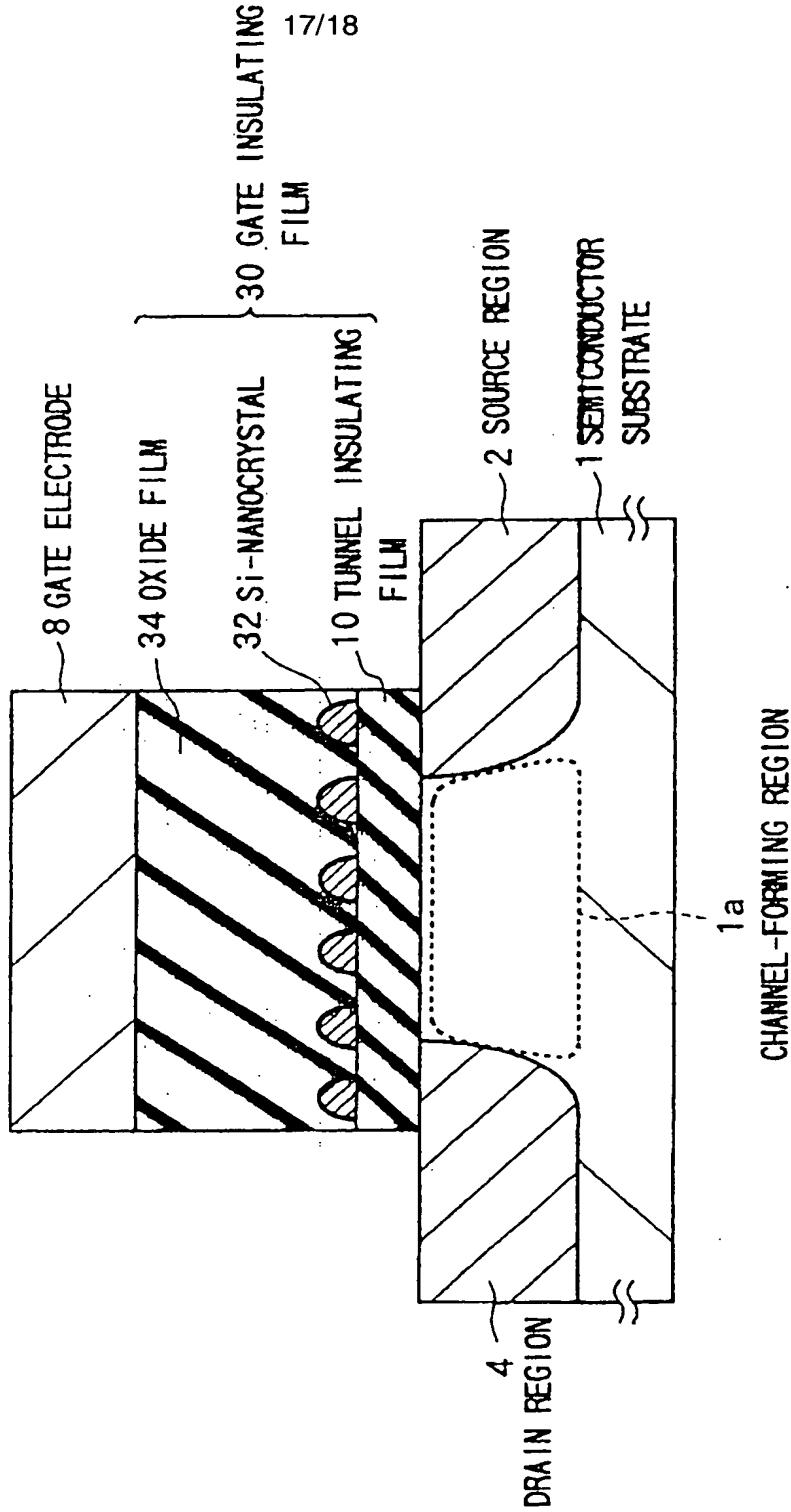


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FIG. 17



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FIG. 18

